

I. Curriculum Vitae

A. Personal Details

Name: Charilaos Mizas

Home Address: A Parodos Paflagonias 4, GR65404 Kavala, Greece

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Date of Birth: February 17, 1971

Birth Place: Thessaloniki, Greece

Marital Status: Married with 2 children

Military Service: Fulfilled

B. Education

1996 : Diploma in Electrical & Computer Engineering, Microelectronics and Information Systems Sector, Department of Electrical & Computer Engineering, Democritus University of Thrace.

2004 : MSc Thesis in Microelectronics and Information Systems, Microelectronics and Information Systems Sector, Department of Electrical & Computer Engineering, Democritus University of Thrace.

C. Member

- Technical Chamber of Greece.
- Panhellenic Union of Electrical and Mechanical Engineers.

D. Employment

1997 - 2019: Researcher at several Research Programs for Electrical and Computer Engineering department, Democritus University of Thrace and for Eastern Macedonia and Thrace Institute of Technology.

1998 – 2000: Network Engineer in Network Information Center of Eastern Macedonia and Thrace Institute of Technology.

1999 - 2000: Co-owner and Manager of Advanced Technology Application Company, ULYSSES GP in Kavala, Greece.

2001 – 2017: Computer Networks and Systems Administrator, IT department, Eastern Macedonia and Thrace Institute of Technology.

2017 – 2019: Special Technical Laboratory Staff, Business Administration Department, Eastern Macedonia and Thrace Institute of Technology

2019 – today: Special Technical Laboratory Staff, Management Science & Technology Department, International Hellenic University.

E. Teaching

i. Current Laboratory Courses

2019 - today: Introduction to Computer Science, Programming I, Programming II, Data Bases.

ii. Past Laboratory Courses

1997 - 1998: "VLSI Systems II", Electrical and Computer Engineering Department, Democritus University of Thrace.

1997 - 1998: "CAD/CAM/CAE Systems", Electrical and Computer Engineering Department, Democritus University of Thrace.

1998 - 1999: "VLSI Systems I", Electrical and Computer Engineering Department, Democritus University of Thrace.

1999 - 2000: "Electrical Circuits", Industrial Informatics Department, Kavala Institute of Technology

1999 - 2000: "Software Programming I & II", Industrial Informatics Department, Kavala Institute of Technology

2001 - 2002: "Computer Networks I", Industrial Informatics Department, Kavala Institute of Technology

2001 - 2012: "Computer Networks II", Industrial Informatics Department, Kavala Institute of Technology

2001 - 2002: "Operating Systems", Industrial Informatics Department, Kavala Institute of Technology

2002 - 2003: "Network Security", Industrial Informatics Department, Kavala Institute of Technology

2005 - 2012: "Internet Information Management", Information Management Department, Kavala and Thrace Institute of Technology

2016 - 2019: "Computer Networks", Business Administration Department, Kavala Institute of Technology

F. Research Interests

Quantum Computing, Nanoelectronics, Design and Simulation of Nanoelectronic Circuits, Quantum Cellular Automata, Computer Networks, Internet Programming, CAD and TCAD Systems, Modeling of VLSI fabrication processes, DNA Computing, systems modeling and simulation, signal processing.

II. List of most significant publications

A. MSc Thesis

"Modeling and Simulation of genetic materials using parallel architectural techniques," Electrical and Computer Engineering Department, Democritus University of Thrace, Xanthi 2004.

B. Journals

- i. G. Ch. Sirakoulis, I. Karafyllidis, Ch. Mizas, V. Mardiris, A. Thanailakis, and Ph. Tsalides, "A cellular automaton model for the study of DNA sequence evolution," *Computers in Biology and Medicine*, vol. 33, no. 5, pp. 439-453, 2003.
- ii. V. Mardiris, G. Ch. Sirakoulis, Ch. Mizas, I. Karafyllidis and A. Thanailakis, "A CAD system for modeling and Simulation of Computer Networks using Cellular Automata," *IEEE Transactions on Systems, Man and Cybernetics – Part C*, vol. 38, no. 2, pp. 253-264, 2008.
- iii. Ch. Mizas, G. Ch. Sirakoulis, V. Mardiris, I. Karafyllidis, N. Glykos and R. Sandaltzopoulos, "Reconstruction of DNA sequences using Genetic Algorithms and Cellular Automata: towards mutation prediction?," *Biosystems*, vol. 92, no. 1, pp. 61-68, 2008.

C. Conferences

- i. P. Tzionas, H. Mizas, A. Thanailakis, "A 2-D window processor for modular image processing applications and its VLSI implementation", 3rd International Workshop in Signal/Image Processing, Advances in Computational Intelligence, Manchester G.B., 1996
- ii. D.Soudris, M.Perakis, Ch.Mizas, V.Mardiris, K.Katis, C.Dre, A.E.Tzimas, E.G.Metaxakis, G.Kalivas, N.Zervas, S.Theoharis, G.Theodoridis, A.Thanailakis, C.Goutis "Low-Power Design of A Multi-Mode Transceiver," *ISCAS 2000*, vol. 2, pp. 721-724, May 28-31, Geneva, Switzerland, 2000.
- iii. L. Frigidis, V. Mardiris, C. Mizas and V. Chatzis, "Examinations Past Papers 'e-Bank' Using Open Source Software", *WSEAS Trans. on Information Science and Applications*, vol. 1, iss. 5, pp. 1220-1224, November 2004.
- iv. L. Frigidis, V. Mardiris, C. Mizas, S. Simeonidis and V. Chatzis, "Electronic Examinations 'e-Exams' Using Open Source Software", *WSEAS Trans. on Information Science and Applications*, vol. 1, iss. 5, pp. 1225-1230, November 2004.
- v. V. Mardiris, G.Ch. Sirakoulis, Ch. Mizas, I. Karafyllidis, and A. Thanailakis, "Net_CA: A Tool for Simulation and Modeling of Wired/Wireless Computer Networks based on Cellular Automata", 10th Panhellenic Conference on Informatics (PCI 2005), pp. 407-417, Volos, Greece, November 11-13, 2005.
- vi. V. Mardiris, Ch. Mizas, L. Frigidis, V. Chatzis, "Design and simulation of a QCA 2 to 1 multiplexer ", *Proceedings of the 12th WSEAS International Conference on Computers*, pp. 572-576, Heraklion, Greece, July 2008.

D. Book Chapters

- i. C. Mizas, G.Ch. Sirakoulis, V. Mardiris, I. Karafyllidis, N. Glykos and R. Sandaltzopoulos, "DNA Cellular Automata," *Designing Beauty: The Art of Cellular Automata*, Springer International Publishing, pp. 127-128, 2016

E. Research Prizes

Prize from Intel and IBM at Low Power Design contest in IEEE 2000 Mesa Computer Elements Workshop, Arizona, 16-19 Jan. 2000, for the project: Soudris D., Zervas N., Perakis M., Mizas X., Mardiris V., Katis K, Dre C., Tzimas A., Metaxakis E., Kalivas G., Theoharis S., Theoharis S., Theodoridis G., Thanailakis A. and Goutis C. με τίτλο "Designing a Low-Power DCS1800 - GSM/DECT Modulator/Demodulator".

III. List other Research Projects

Researcher in:

- i. "Low-power VLSI design: high-end power consumption estimation for digital processing applications", TSMEDE.
- ii. "A Low-Power Design Methodology/Flow and its Application to the Implementation of a DCS 1800 –GSM/DECT Modulator – Demodulator", LPGD 25256.
- iii. "Development and Utilization of Modern Automatic Control Methods in Electricity Production Systems", ARCHIMEDES I